mDOM\_mb3, #40, Test Log

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Mainboard-ID | HH-ZE-ID | ICM\_4-Nr. | ICM-ID | mDAB\_2-Nr. | STF | HV-ctrl | remarks |
| ed00000067359842 | 13128-01 40 | 0539 | 9a000030c34fab2d | 60 |  |  | **FPGA with blanked-out ser.nr, speed grade and temp. grade** |

Slow ADC readout, after STF:

In [3]: print(session.sloAdcReadAll())

channel 0 P5V\_IMON 1482.7 mA

channel 1 P3V3A\_IMON 317.85 mA

channel 2 P1V8\_IMON 1205.7 mA

channel 3 P1V8A\_IMON 767.33 mA

channel 4 P1V35\_IMON 338.79 mA

channel 5 P1V0\_IMON 754.85 mA

channel 6 HVVDDSW\_IMON 8.4038 mA

channel 7 HVVDDU\_IMON 3.9101 mA

channel 8 P5V\_CAL\_IMON 0.88501 mA

channel 9 P3V3\_CAL\_IMON 2.079 mA

channel 10 P1V8\_CAL\_IMON 7.4005 mA

channel 11 P5V\_VMON 4.9582 V

channel 12 P3V3\_VMON 3.2794 V

channel 13 P1V8\_VMON 1.8157 V

channel 14 P1V35\_VMON 1.3374 V

channel 15 P1V0\_VMON 0.98087 V