mDOM\_mb3, #39, Test Log

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Mainboard-ID | HH-ZE-ID | ICM\_4-Nr. | ICM-ID | mDAB\_2-Nr. | STF | HV-ctrl | remarks |
| 2000000067358142 | 13128-01 39 | 0540 | e4000030c2fcec2d | 59 | passed |  | **FPGA with blanked-out ser.nr, speed grade and temp. grade, taken from the second (60pcs) batch** |

Slow ADC readout, after STF:

In [3]: print(session.sloAdcReadAll())

channel 0 P5V\_IMON 1476.6 mA

channel 1 P3V3A\_IMON 307.9 mA

channel 2 P1V8\_IMON 1229.7 mA

channel 3 P1V8A\_IMON 799.87 mA

channel 4 P1V35\_IMON 304.37 mA

channel 5 P1V0\_IMON 719.76 mA

channel 6 HVVDDSW\_IMON 0.0076294 mA

channel 7 HVVDDU\_IMON 5.8594 mA

channel 8 P5V\_CAL\_IMON 1.0414 mA

channel 9 P3V3\_CAL\_IMON 2.5711 mA

channel 10 P1V8\_CAL\_IMON 1.1177 mA

channel 11 P5V\_VMON 4.9903 V

channel 12 P3V3\_VMON 3.2987 V

channel 13 P1V8\_VMON 1.8188 V

channel 14 P1V35\_VMON 1.3363 V

channel 15 P1V0\_VMON 0.97996 V