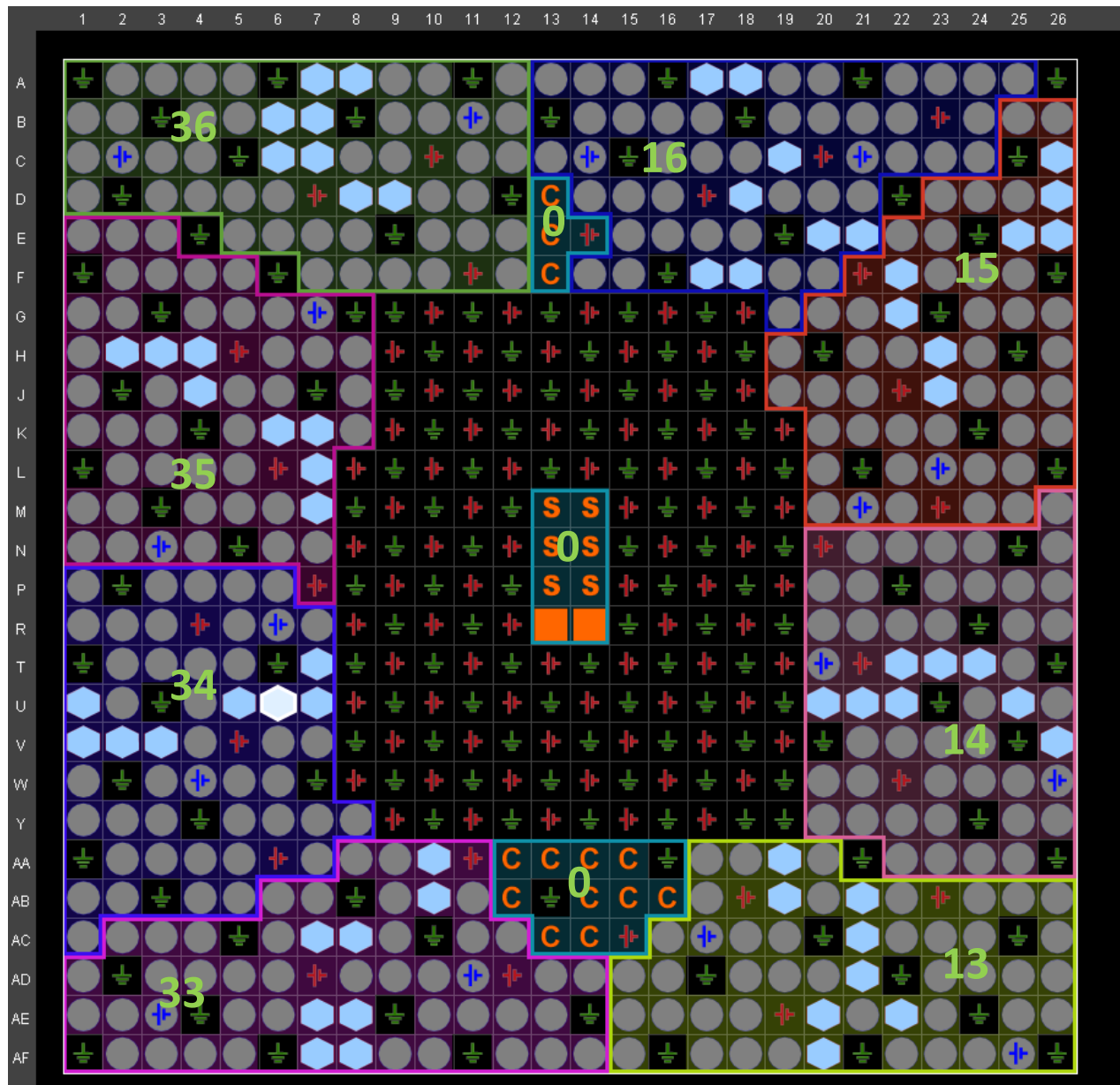


XC7S100-2FGGA676I banks, top view



Layers | General

Q Z

- ☒ I/O Ports
 - ☒ Placed Ports
 - ☒ Fixed Ports
- ☒ Pins
 - ☒ Power Supply Pins
 - ☒ I/O and Multi-Function Pins
 - ☒ Dedicated XADC Pins
 - ☒ Dedicated Configuration Pins
 - ☒ Temperature Sensor Pins
- ☐ I/O Bank Types
 - ☐ High Performance
 - ☐ High Range
 - ☐ High Density
 - ☐ Dedicated
 - ☐ MGT
 - ☐ PSS
 - ☐ ADC
 - ☐ DAC
- ☒ I/O Banks
 - ☒ I/O Bank 0
 - ☒ I/O Bank 13
 - ☒ I/O Bank 14
 - ☒ I/O Bank 15
 - ☒ I/O Bank 16
 - ☒ I/O Bank 33
 - ☒ I/O Bank 34
 - ☒ I/O Bank 35
 - ☒ I/O Bank 36
- ☒ Other
 - ☒ Grid