

mDOM mainboard, MCU signals shared with the FPGA

The default is tristate for these signals.

Rev.1

Signal	FPGA-Pin	remark
MCU_USART6_TX	F4	
MCU_USART6_RX	F3	
MCU_CAL_A0	F14	
MCU_AFE_SEL	D15	
MCU_CAL_SS	D14	
MCU_CAL_MOSI	C14	
MCU_CAL_MISO	C13	

rev.2

Signal	FPGA-Pin	remark
MCU_USART6_TX	F4	
MCU_USART6_RX	F3	
MCU_CAL_A0	F14	
MCU_CAL_A1	A13	new
MCU_CAL_A2	A15	new
MCU_CAL_A3	C18	new
MCU_CAL_A4	B15	new
MCU_AFE_SEL	D15	
MCU_CAL_SS	D14	
MCU_CAL_MOSI	C14	
MCU_CAL_MISO	C13	