

#### INFORMATIONEN

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Firma : DESY Zeuthen  
Lp.-Nr. : FDOR-9485-00  
  
Stand : 13.01.2022  
  
LP.-Mass : 100 mm x 290,5 mm x 1,57 +/- 0,13 mm  
Lagenzahl : 8

- Die Strukturen auf der Leiterplatte muessen den Strukturen in den Gerberdaten entsprechen.
- Der angegebene Lochdurchmesser im Bohrbild entspricht dem Endlochdurchmesser.

FOTOPLOTT:  
Format : Gerber RS 274 X

BOHRFILE:  
Format : Excellon  
Masseinheit : metrisch

9485-00\_L01T.ger Layer 1 - Top / GND / Signale / Differentielle Leitungen 100ohm, 150 Ohm  
9485-00\_L02i.ger Layer 2 - GND  
9485-00\_L03i.ger Layer 3 - Power / GND / Signale / Differentielle Leitungen 100 Ohm  
9485-00\_L04i.ger Layer 4 - GND  
9485-00\_L05i.ger Layer 5 - Power  
9485-00\_L06i.ger Layer 6 - Signale  
9485-00\_L07i.ger Layer 7 - Power / GND  
9485-00\_L08B.ger Layer 8 - Bottom / GND / Signale  
9485-00\_LSM1.ger Loetstopp Layer 1  
9485-00\_LSM2.ger Loetstopp Layer 8  
9485-00\_SMD1.ger SMD-Paste Layer 1  
9485-00\_SMD2.ger SMD-Paste Layer 8  
9485-00\_POS1.ger Best.-Druck Layer 1  
9485-00\_POS2.ger Best.-Druck Layer 8  
9485-00\_BOHR.ger Bohrbild

ThruHolePlated.ncd ThruHolePlated  
ThruHoleNonPlated.ncd ThruHoleNonPlated  
9485-00\_Bohr.pdf Bohrbild

9485-00\_Lagenaufbau.pdf Vorschlag-Lagenaufbau

# 150 ohm Impedanzberechnung

Saturn PCB Design, Inc. - PCB Toolkit V7.02 - www.saturnpcb.com

File Program Function Tools Help | Contact Saturn PCB Design, Inc.

Conductor Spacing Conductor Impedance Conversion Data Planar Inductors Plane Calculator Thermal Fusing Current  
Embedded Resistors PPM Calculator Crosstalk Calculator Wavelength Calculator Er Effective Ohm's Law Reactance  
Via Properties Conductor Properties Bandwidth & Max Conductor Length Differential Pairs Padstack Calculator Mechanical Information

**Differential Pairs**

Conductor Width (W)  mm Target Zdiff  Ohms

Conductor Spacing (S)  mm

Conductor Height (H)  mm

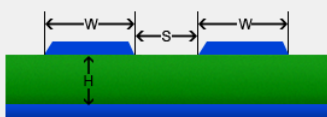
$W/H = 0.361$   
 $S/H = 0.639$

Formula Restrictions:  
 $0.1 < W/H < 3.0$   
 $0.1 < S/H < 3.0$

Zdifferential  Ohms

Zo  Ohms

+/- Tolerance = 10%  
 Ohms  
 Ohms



**Options**

Base Copper Weight  
☒ 9um  
☐ 18um  
☐ 35um  
☐ 53um  
☐ 70um  
☐ 88um  
☐ 106um  
☐ 142um  
☐ 178um

Units  
☐ Imperial  
☒ Metric

Substrate Options  
Material Selection

Er  Tg (°C)

Plating Thickness  
☐ Bare PCB  
☐ 18um  
☒ 35um  
☐ 53um  
☐ 70um  
☐ 88um  
☐ 106um

Differential Layer  
☒ Edge CpId Ext  
☐ Edge CpId Int Sym  
☐ Edge CpId Int Asym  
☐ Edge CpId Embed  
☐ Broad CpId Shld  
☐ Broad CpId NShld

Temp Rise (°C)   
Temp in (°F) = 36.0

Ambient Temp (°C)   
Temp in (°F) = 71.6

Print Solve!

**Information**

Total Copper Thickness  
44 um

Via Thermal Resistance  
N/A

Via Count:

Conductor Temperature  
Temp in (°C) = N/A  
Temp in (°F) = N/A

Via Voltage Drop  
N/A

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