# FDOR\_1, Firmware Readme

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| Rev. | .mcs / .bit File name | Remark |
| 0 | FDOR\_1\_00 | Communication and power switching for all 4 wire pairs.  Still missing:  Clock switching,  current / voltage monitoring,  Golden image handling + FPGA reload |
| 1 | FDOR\_1\_01 | Communication and power switching for all 4 wire pairs.  Including:  current / voltage monitoring,19.0738uA/bit, 3.8147mV/bit  Still missing:  Clock switching  Golden image handling + FPGA reload |
| 2 | FDOR\_1\_02 | Communication and power switching for all 4 wire pairs.  Including:  current / voltage monitoring, 19.0738uA/bit, 3.8147mV/bit  Clock switching  Still missing:  Golden image handling + FPGA reload |
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