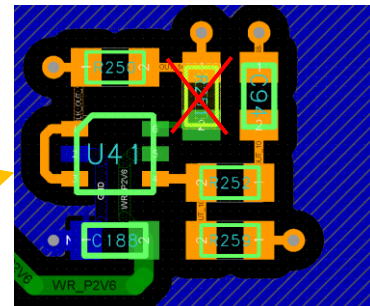
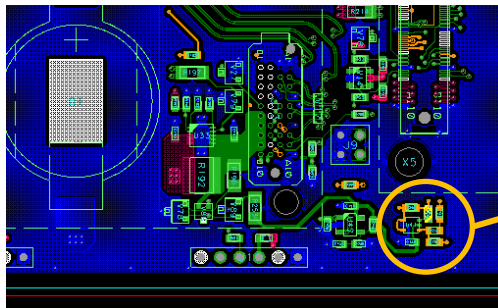
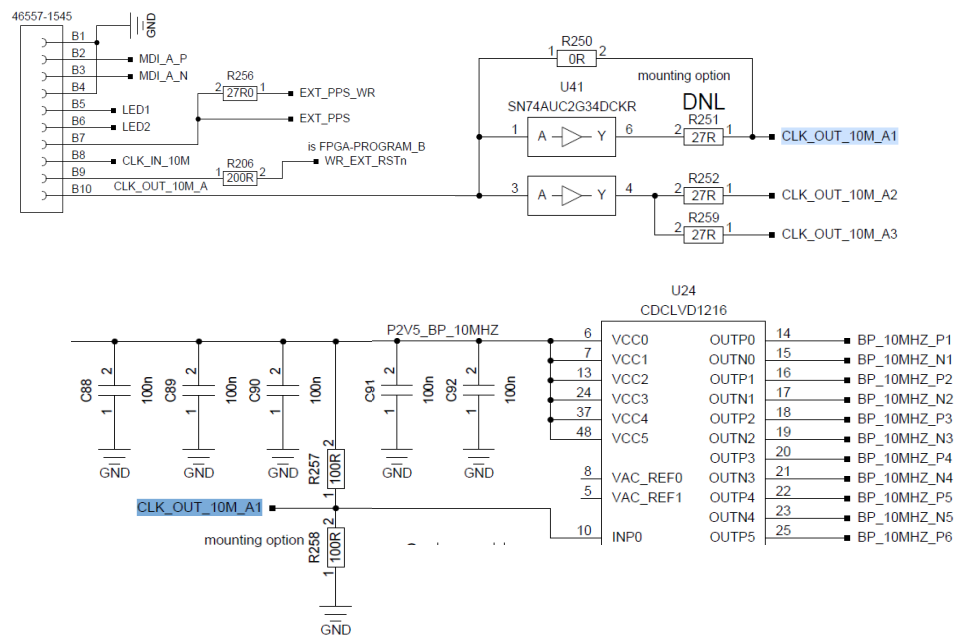


FCON_2, improving the 10MHz and IRIGB Signals

10MHZ, provided by the WR-LEN

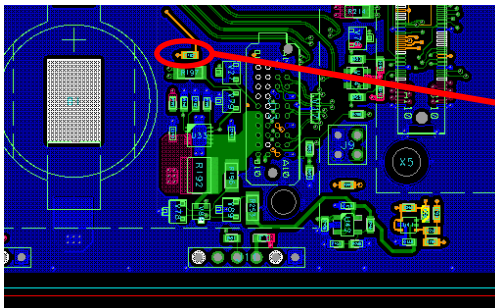
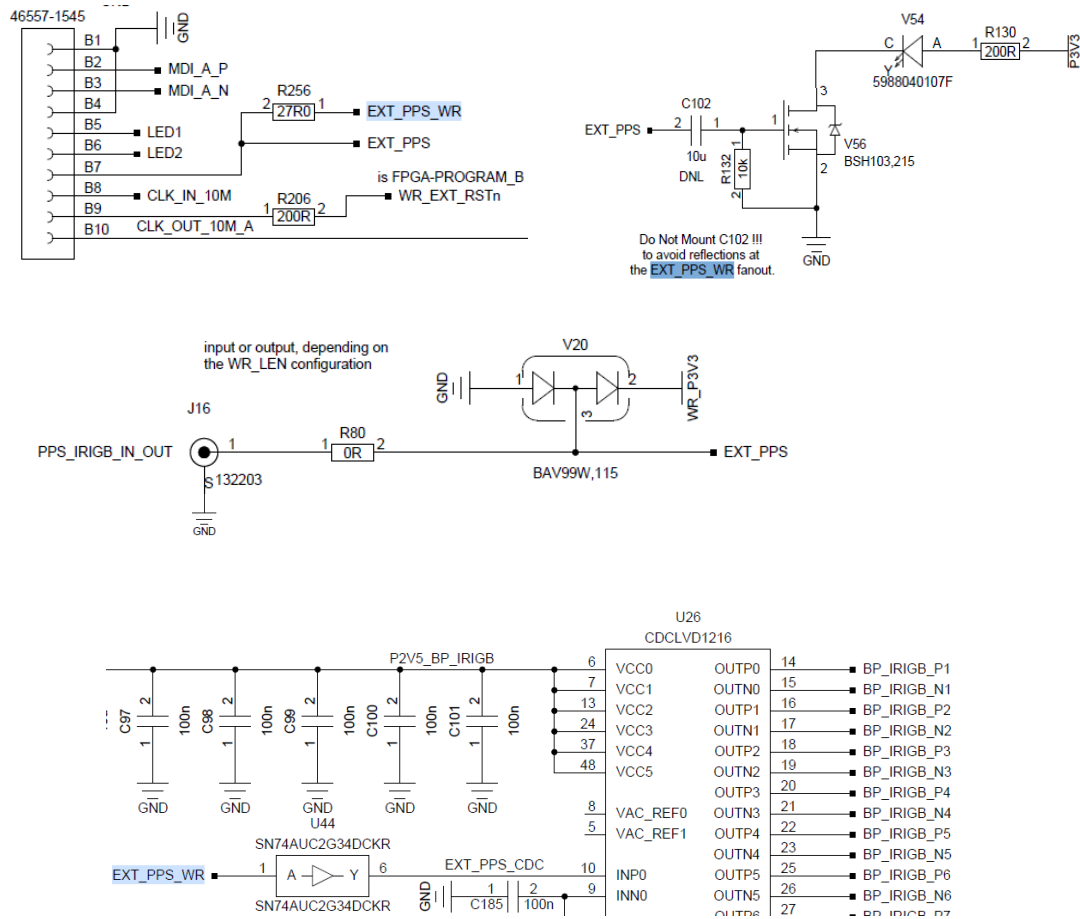
To improve the signal shape at the 10MHZ-fanout U24 and to get rid off the buffer U41 chip to chip skew. See below the 10MHz from WR-LEN to U24.

1. Remove R251, mount R250 = 0R / 0603 instead



IRIGB, provided by the WR-LEN

To improve the signal shape at the IRIGB-fanout U26 and to get rid off reflections, C102 has to be removed. To be considered is the influence of the signal termination at the SMA connector J16 on the signal high-level at the fanout driver U44. A minimum high-level of 1.7V is required.



IRIGB at SMA with 50ohm load and 2.5V-fanout-driver-input, measured at **FCON_1**. High level of 1.8V is achieved here, should be the same for FCON_2.

