# IRIGB\_enc Firmware Properties

CPLD: XC2C64A-6VQG44C

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| **Revision** | **Name** | **Short Description** |
| 1 | IRIGB\_enc\_1.jed | Generates 10MHz from a 20MHz oscillator (ML602-020.0M).  Generates a static IRIG-B string, first pulse is 9ms instead of 8ms for convenient scope-triggering.  The static part:  191 days  14 hours  08 minutes  32 seconds  Only the SBS-part (straight binary seconds of the day , last 200ms of the second) is dynamic,  starting at 0 after power-on.  Encoding:  8ms = 0.1 second position identifier (start)  9ms = start of the string (just for testing, should be 8ms)  2ms = logic ‘0’  5ms = logic ‘1’ |
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