# Firmware Download Link: <https://www-zeuthen.desy.de/~sulanke/Projects/CTA/Dig_trigger/NectarCam/L2/CTDB/rev2/firmware/>

# CTDB2 Firmware Properties

FPGA: Xilinx XC6SLX25-2FGG484C

FLASH (SPI, 8 Mbit): AT45DB081E-SSHN

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| **Revision** | **Name** | **Clock source** | **VHDL-source** | **Short Description** |
| 001 | ctdb2\_001 | Local clock and L2CB-clock | ctdb2\_001.vhd | 15 channel clock fanout, trigger reception, L1A trigger distribution, FE-board power on/off via SPI driven by the L2CB, current monitoring, electronic fuse by firmware |
| 003 | ctdb2\_003 | Local clock and L2CB-clock |  | Like 001, but distribute 66MHz now. Use with the appropriate 66MHz version of the L2CB firmware. |