

J11 : DO NOT MOUNT

J6 : DO NOT MOUNT

J7 : DO NOT MOUNT

J13 : DO NOT MOUNT

Developer:	Karl-Heinz Sulanke	Project: CTA, Digital Trigger, L2				
Drawn by:	Karl-Heinz Sulanke	Schematic: CTDB, Clock & Trigger Distribution Board				
		Sheet: DC / DC power supply, JTAG port, optional TIB connectors, etc.				
Layouter:	ANAKONDA ELECTRONIC DESIGN	DESY- Zeuthen-EL		Platanenallee 6 D-15738 Zeuthen		
Changed of sch:	<CHANGED BY>					
Date Changed:	08/01/2018:08:24	Board Nr:	<NR>	Rev:	2	Size: A3
Date of prod. data:	<DATE>	Drawing Nr:	<DRAW NR>	Sheet:	1	of 4





